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## Inventor Name Search Result

Your Search was:

Last Name = BARR

First Name = ANDREW

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10370537</a>	6970001	150	02/20/2003	VARIABLE IMPEDANCE TEST PROBE	BARR, ANDREW
<a href="#">10684646</a>	Not Issued	71	10/14/2003	Power distribution system	BARR, ANDREW
<a href="#">06651047</a>	4628302	150	09/14/1984	LIQUID LEVEL DETECTION SYSTEM	BARR, ANDREW B.
<a href="#">11164200</a>	Not Issued	30	11/14/2005	SEAT IDENTIFICATION SYSTEM	BARR, ANDREW D.
<a href="#">07129437</a>	5173699	150	11/05/1987	ANTENNA ARRANGEMENT	BARR, ANDREW D.
<a href="#">07167485</a>	Not Issued	89	02/22/1988	RADAR SYSTEM	BARR, ANDREW D.
<a href="#">07833296</a>	Not Issued	161	02/11/1992	FLUID MONITORING APPARATUS	BARR, ANDREW D.
<a href="#">09923735</a>	6559733	150	08/06/2001	REDUCING EFFECTS OF ELECTRICAL IMPEDANCE	BARR, ANDREW H.
<a href="#">10216229</a>	Not Issued	71	08/12/2002	System and method for managing the operating frequency of blades in a bladed-system	BARR, ANDREW H.
<a href="#">10216232</a>	7055044	150	08/12/2002	SYSTEM AND METHOD FOR VOLTAGE MANAGEMENT OF A PROCESSOR TO OPTIMIZE PERFORMANCE AND POWER DISSIPATION	BARR, ANDREW H.
<a href="#">10216233</a>	7080263	150	08/12/2002	VOLTAGE MANAGEMENT OF PROCESSORS IN A BLADED SYSTEM BASED ON NUMBER OF LOADED PROCESSORS	BARR, ANDREW H.
<a href="#">10216234</a>	Not Issued	71	08/12/2002	System and method for load dependent frequency and performance modulation in bladed systems	BARR, ANDREW H.
<a href="#">10216283</a>	7076671	150	08/12/2002	MANAGING AN OPERATING FREQUENCY OF PROCESSORS IN A MULTI-PROCESSOR COMPUTER SYSTEM	BARR, ANDREW H.
<a href="#">10216284</a>	6983386	150	08/12/2002	VOLTAGE MANAGEMENT OF	BARR, ANDREW H.

				BLADES IN A BLADED ARCHITECTURE SYSTEM BASED ON THERMAL AND POWER BUDGET ALLOCATION	
<u>10216285</u>	Not Issued	100	08/12/2002	BLADE SYSTEM FOR USING MULTIPLE FREQUENCY SYNTHESIZERS TO CONTROL MULTIPLE PROCESSOR CLOCKS OPERATING AT DIFFERENT FREQUENCIES BASED UPON USER INPUT	BARR, ANDREW H.
<u>10216286</u>	Not Issued	95	08/12/2002	SYSTEM AND METHOD FOR MANAGING PROCESSOR VOLTAGE IN A MULTI-PROCESSOR COMPUTER SYSTEM FOR OPTIMIZED PERFORMANCE	BARR, ANDREW H.
<u>10216435</u>	6948043	150	08/12/2002	MANAGEMENT OF A MEMORY SUBSYSTEM	BARR, ANDREW H.
<u>10216437</u>	7058828	150	08/12/2002	SYSTEM, METHOD AND APPARATUS FOR THE FREQUENCY MANAGEMENT OF BLADES IN A BLADED ARCHITECTURE BASED ON PERFORMANCE REQUIREMENTS	BARR, ANDREW H.
<u>10216438</u>	Not Issued	30	08/12/2002	System and method for the frequency management of computer systems to allow capacity on demand	BARR, ANDREW H.
<u>10328805</u>	Not Issued	41	12/23/2002	Enabling multiple testing devices	BARR, ANDREW H.
<u>10328906</u>	Not Issued	71	12/23/2002	Automatic detection of different microprocessor architectures	BARR, ANDREW H.
<u>10358903</u>	Not Issued	120	02/05/2003	Method and apparatus for improving signal integrity in a high speed flex cable	BARR, ANDREW H.
<u>10453594</u>	Not Issued	161	06/04/2003	Apparatus and method for detecting and rejecting high impedance interconnect failures in manufacturing process	BARR, ANDREW H.
<u>10453595</u>	6940288	150	06/04/2003	APPARATUS AND METHOD FOR MONITORING AND PREDICTING FAILURES IN SYSTEM INTERCONNECT	BARR, ANDREW H.
<u>10453610</u>	6895353	150	06/04/2003	APPARATUS AND METHOD FOR MONITORING HIGH IMPEDANCE FAILURES IN CHIP INTERCONNECTS	BARR, ANDREW H.
<u>10453612</u>	6879173	150	06/04/2003	APPARATUS AND METHOD FOR DETECTING AND REJECTING HIGH IMPEDANCE FAILURES IN CHIP INTERCONNECTS	BARR, ANDREW H.
<u>10453613</u>	Not Issued	164	06/04/2003	APPARATUS AND METHOD FOR DETECTING HIGH IMPEDANCE	BARR, ANDREW H.

				FAILURES IN SYSTEM INTERCONNECT	
<a href="#">10459231</a>	<a href="#">6791843</a>	150	06/11/2003	PARALLEL BOARD CONNECTION SYSTEM AND METHOD	BARR, ANDREW H.
<a href="#">10622917</a>	<a href="#">6957544</a>	150	07/18/2003	METHOD AND APPARATUS FOR REGULATING THE OPERATING TEMPERATURE OF ELECTRONIC DEVICES	BARR, ANDREW H.
<a href="#">10631696</a>	<a href="#">6961242</a>	150	07/31/2003	SYSTEM FAN MANAGEMENT BASED ON SYSTEM LOADING OPTIONS FOR A SYSTEM HAVING REPLACEABLE ELECTRONICS MODULES	BARR, ANDREW H.
<a href="#">10632218</a>	Not Issued	20	07/31/2003	Heat sink fan management based on performance requirements	BARR, ANDREW H.
<a href="#">10646078</a>	Not Issued	71	08/22/2003	Bus clock frequency management based on characteristics of an application program	BARR, ANDREW H.
<a href="#">10646079</a>	Not Issued	30	08/22/2003	Bus clock frequency management based on device load	BARR, ANDREW H.
<a href="#">10646099</a>	Not Issued	90	08/22/2003	BUS CLOCK FREQUENCY MANAGEMENT BASED ON DEVICE BANDWIDTH CHARACTERISTICS	BARR, ANDREW H.
<a href="#">10652536</a>	Not Issued	61	08/29/2003	System and method for testing a memory	BARR, ANDREW H.
<a href="#">10698204</a>	<a href="#">6985826</a>	150	10/31/2003	SYSTEM AND METHOD FOR TESTING A COMPONENT IN A COMPUTER SYSTEM USING VOLTAGE MARGINING	BARR, ANDREW H.
<a href="#">10699423</a>	Not Issued	61	10/31/2003	System and method for testing a cell	BARR, ANDREW H.
<a href="#">10703306</a>	Not Issued	41	11/07/2003	System and method for testing a component in a computer system using frequency margining	BARR, ANDREW H.
<a href="#">10714302</a>	Not Issued	30	11/14/2003	System and method for testing a memory using DMA	BARR, ANDREW H.
<a href="#">10714386</a>	Not Issued	61	11/14/2003	System and method for testing a memory with an expansion card using DMA	BARR, ANDREW H.
<a href="#">10727440</a>	<a href="#">7072788</a>	150	12/04/2003	SYSTEM AND METHOD FOR TESTING AN INTERCONNECT IN A COMPUTER SYSTEM	BARR, ANDREW H.
<a href="#">11033751</a>	<a href="#">6995581</a>	150	01/13/2005	APPARATUS AND METHOD FOR DETECTING AND REJECTING HIGH IMPEDANCE FAILURES IN CHIP INTERCONNECTS	BARR, ANDREW H.
<a href="#">11109309</a>	Not Issued	30	04/19/2005	Redundant I/O interface management	BARR, ANDREW H.
<a href="#">11109352</a>	Not Issued	20	04/19/2005	Computing with both lock-step and free-step processor modes	BARR, ANDREW H.

<a href="#">11109353</a>	Not Issued	30	04/19/2005	External state cache for computer processor	BARR, ANDREW H.
<a href="#">11191052</a>	Not Issued	20	07/28/2005	System fan management based on system loading options for a system having replaceable electronics modules	BARR, ANDREW H.
<a href="#">10263985</a>	<a href="#">6970054</a>	150	10/02/2002	APPARATUS FOR TERMINATING TRANSMISSION LINES TO REDUCE ELECTROMAGNETIC INTERFERENCE IN AN ELECTRONIC SYSTEM	BARR, ANDREW HARVEY
<a href="#">10298876</a>	Not Issued	161	11/18/2002	Cable systems and related methods	BARR, ANDREW HARVEY
<a href="#">10308533</a>	<a href="#">6676417</a>	150	12/03/2002	SYSTEMS AND METHODS FOR INTERCONNECTING ELECTRONIC COMPONENTS	BARR, ANDREW HARVEY
<a href="#">10351950</a>	<a href="#">6680853</a>	150	01/27/2003	SYSTEMS AND METHODS FOR MOUNTING COMPONENTS TO CIRCUIT ASSEMBLIES	BARR, ANDREW HARVEY

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## Inventor Name Search Result

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First Name = RICARDO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10216229</a>	Not Issued	71	08/12/2002	System and method for managing the operating frequency of blades in a bladed-system	ESPINOZA-IBARRA, RICARDO
<a href="#">10216232</a>	<a href="#">7055044</a>	150	08/12/2002	SYSTEM AND METHOD FOR VOLTAGE MANAGEMENT OF A PROCESSOR TO OPTIMIZE PERFORMANCE AND POWER DISSIPATION	ESPINOZA-IBARRA, RICARDO
<a href="#">10216233</a>	<a href="#">7080263</a>	150	08/12/2002	VOLTAGE MANAGEMENT OF PROCESSORS IN A BLADED SYSTEM BASED ON NUMBER OF LOADED PROCESSORS	ESPINOZA-IBARRA, RICARDO
<a href="#">10216234</a>	Not Issued	71	08/12/2002	System and method for load dependent frequency and performance modulation in bladed systems	ESPINOZA-IBARRA, RICARDO
<a href="#">10216283</a>	<a href="#">7076671</a>	150	08/12/2002	MANAGING AN OPERATING FREQUENCY OF PROCESSORS IN A MULTI-PROCESSOR COMPUTER SYSTEM	ESPINOZA-IBARRA, RICARDO
<a href="#">10216284</a>	<a href="#">6983386</a>	150	08/12/2002	VOLTAGE MANAGEMENT OF BLADES IN A BLADED ARCHITECTURE SYSTEM BASED ON THERMAL AND POWER BUDGET ALLOCATION	ESPINOZA-IBARRA, RICARDO
<a href="#">10216285</a>	Not Issued	100	08/12/2002	BLADE SYSTEM FOR USING MULTIPLE FREQUENCY SYNTHESIZERS TO CONTROL MULTIPLE PROCESSOR CLOCKS OPERATING AT DIFFERENT FREQUENCIES BASED UPON USER INPUT	ESPINOZA-IBARRA, RICARDO
<a href="#">10216286</a>	Not Issued	95	08/12/2002	SYSTEM AND METHOD FOR MANAGING PROCESSOR VOLTAGE IN A MULTI-PROCESSOR COMPUTER SYSTEM FOR OPTIMIZED PERFORMANCE	ESPINOZA-IBARRA, RICARDO
<a href="#">10216435</a>	<a href="#">6948043</a>	150	08/12/2002	MANAGEMENT OF A MEMORY SUBSYSTEM	ESPINOZA-IBARRA, RICARDO

<a href="#">10216437</a>	<a href="#">7058828</a>	150	08/12/2002	SYSTEM, METHOD AND APPARATUS FOR THE FREQUENCY MANAGEMENT OF BLADES IN A BLADED ARCHITECTURE BASED ON PERFORMANCE REQUIREMENTS	ESPINOZA-IBARRA, RICARDO
<a href="#">10216438</a>	Not Issued	30	08/12/2002	System and method for the frequency management of computer systems to allow capacity on demand	ESPINOZA-IBARRA, RICARDO
<a href="#">10263587</a>	Not Issued	61	10/02/2002	Apparatus for controlling transmissions to reduce electromagnetic interference in an electronic system	ESPINOZA-IBARRA, RICARDO
<a href="#">10263985</a>	<a href="#">6970054</a>	150	10/02/2002	APPARATUS FOR TERMINATING TRANSMISSION LINES TO REDUCE ELECTROMAGNETIC INTERFERENCE IN AN ELECTRONIC SYSTEM	ESPINOZA-IBARRA, RICARDO
<a href="#">10360329</a>	Not Issued	61	02/07/2003	Radio frequency linked computer architecture	ESPINOZA-IBARRA, RICARDO
<a href="#">10631696</a>	<a href="#">6961242</a>	150	07/31/2003	SYSTEM FAN MANAGEMENT BASED ON SYSTEM LOADING OPTIONS FOR A SYSTEM HAVING REPLACEABLE ELECTRONICS MODULES	ESPINOZA-IBARRA, RICARDO
<a href="#">10632218</a>	Not Issued	20	07/31/2003	Heat sink fan management based on performance requirements	ESPINOZA-IBARRA, RICARDO
<a href="#">10646078</a>	Not Issued	71	08/22/2003	Bus clock frequency management based on characteristics of an application program	ESPINOZA-IBARRA, RICARDO
<a href="#">10646079</a>	Not Issued	30	08/22/2003	Bus clock frequency management based on device load	ESPINOZA-IBARRA, RICARDO
<a href="#">10646099</a>	Not Issued	90	08/22/2003	BUS CLOCK FREQUENCY MANAGEMENT BASED ON DEVICE BANDWIDTH CHARACTERISTICS	ESPINOZA-IBARRA, RICARDO
<a href="#">10653377</a>	<a href="#">7054156</a>	150	09/02/2003	FAN ROTOR SYSTEMS HAVING COLLAPSIBLE FAN BLADES	ESPINOZA-IBARRA, RICARDO
<a href="#">10678464</a>	Not Issued	30	10/03/2003	Rack equipment management information coordination system and method	ESPINOZA-IBARRA, RICARDO
<a href="#">10678657</a>	Not Issued	71	10/03/2003	Rack equipment management system and method	ESPINOZA-IBARRA, RICARDO
<a href="#">10681721</a>	Not Issued	71	10/07/2003	Rack equipment application performance modification system and method	ESPINOZA-IBARRA, RICARDO
<a href="#">10741906</a>	Not Issued	30	12/18/2003	Rack equipment power purchase plan supervision system and method	ESPINOZA-IBARRA, RICARDO
<a href="#">10742495</a>	Not Issued	30	12/18/2003	Equipment rack load modulation system and method	ESPINOZA-IBARRA, RICARDO
<a href="#">10829072</a>	Not Issued	40	04/20/2004	Rack equipment capacity on demand system and method	ESPINOZA-IBARRA, RICARDO
<a href="#">11033083</a>	Not Issued	41	01/10/2005	Dynamically adaptable electronics cooling fan	ESPINOZA-IBARRA, RICARDO

<a href="#">11260095</a>	Not Issued	20	10/26/2005	Electronics cooling fan with collapsible fan blade	ESPINOZA-IBARRA, RICARDO
<a href="#">11260105</a>	Not Issued	20	10/26/2005	Centrifugal fan clutch for an electronics cooling fan	ESPINOZA-IBARRA, RICARDO
<a href="#">10452222</a>	Not Issued	61	06/03/2003	Apparatus and method for mounting a surface mount component in an etched well in a printed circuit board	ESPINOZA-IBARRA, RICARDO E.
<a href="#">10602972</a>	<a href="#">6987370</a>	150	06/23/2003	METHOD AND SYSTEM FOR COOLING ELECTRONIC COMPONENTS	ESPINOZA-IBARRA, RICARDO E.
<a href="#">10612523</a>	Not Issued	30	07/02/2003	Apparatus and method for real-time power distribution management	ESPINOZA-IBARRA, RICARDO E.
<a href="#">10684768</a>	Not Issued	41	10/14/2003	Server card power switch	ESPINOZA-IBARRA, RICARDO E.
<a href="#">10685175</a>	Not Issued	41	10/14/2003	Power distribution system	ESPINOZA-IBARRA, RICARDO E.
<a href="#">10769049</a>	Not Issued	161	01/29/2004	Information filtering device	ESPINOZA-IBARRA, RICARDO E.
<a href="#">10801247</a>	Not Issued	30	03/15/2004	Rack equipment power pricing plan control system and method	ESPINOZA-IBARRA, RICARDO E.
<a href="#">10623015</a>	Not Issued	161	07/17/2003	Method and system for independently displaying a plurality of visual signals	ESPINOZA-IBARRA, RICARDO ERNESTO
<a href="#">10698904</a>	<a href="#">6956344</a>	150	10/31/2003	HIGH AVAILABILITY FAN SYSTEM	ESPINOZA-IBARRA, RICARDO ERNESTO
<a href="#">10699430</a>	Not Issued	30	10/31/2003	Scalable, modular, high availability fan system	ESPINOZA-IBARRA, RICARDO ERNESTO

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# 1 Energy constraints on parameterized models



Andrew Witkin, Kurt Fleischer, Alan Barr

 August 1987 **ACM SIGGRAPH Computer Graphics , Proceedings of the 14th annual conference on Computer graphics and interactive techniques SIGGRAPH '87**, Volume 21 Issue 4

Publisher: ACM Press

Full text available: pdf(3.73 MB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A simple but general approach to imposing and solving geometric constraints on parameterized models is introduced, applicable to animation as well as model construction. Constraints are expressed as energy functions, and the energy gradient followed through the model's parameter space. Intuitively, energy constraints behave like forces that pull and parametrically deform the parts of the model into place. A wide variety of geometric constraints are amenable to this formulation, and may be used t ...

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### 1 [Temperature and power aware architectures: Reducing power density through activity migration](#)



Seongmoo Heo, Kenneth Barr, Krste Asanović

 August 2003 **Proceedings of the 2003 international symposium on Low power electronics and design**

Publisher: ACM Press

Full text available: pdf(144.76 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Power dissipation is unevenly distributed in modern microprocessors leading to localized hot spots with significantly greater die temperature than surrounding cooler regions. Excessive junction temperature reduces reliability and can lead to catastrophic failure. We examine the use of activity migration which reduces peak junction temperature by moving computation between multiple replicated units. Using a thermal model that includes the temperature dependence of leakage power, we show that sust ...

**Keywords:** activity migration, temperature reduction, thermal model

### 2 [Energy aware lossless data compression](#)



Kenneth Barr, Krste Asanović

 May 2003 **Proceedings of the 1st international conference on Mobile systems, applications and services MobiSys '03**

Publisher: ACM Press

Full text available: pdf(299.94 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#)

Wireless transmission of a bit can require over 1000 times more energy than a single 32-bit computation. It would therefore seem desirable to perform significant computation to reduce the number of bits transmitted. If the energy required to compress data is less than the energy required to send it, there is a net energy savings and consequently, a longer battery life for portable computers. This paper reports on the energy of lossless data compressors as measured on a StrongARM SA-110 system. W ...

### 3 [Operating systems for sensor networks: Design and implementation of a single system image operating system for ad hoc networks](#)



Hongzhou Liu, Tom Roeder, Kevin Walsh, Rimon Barr, Emin Gün Sirer

 June 2005 **Proceedings of the 3rd international conference on Mobile systems, applications, and services MobiSys '05**

Publisher: ACM Press

Full text available: pdf(261.28 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#)

In this paper, we describe the design and implementation of a distributed operating system for ad hoc networks. Our system simplifies the programming of ad hoc networks


and extends total system lifetime by making the entire network appear as a single virtual machine. It automatically and transparently partitions applications into components and dynamically finds them a placement on nodes within the network to reduce energy consumption and to increase system longevity. This paper describes our pr ...

#### 4 Embedded systems design for low energy consumption

Michael A. Schuette, John R. Barr

November 1994 **Proceedings of the 1994 IEEE/ACM international conference on Computer-aided design**

**Publisher:** IEEE Computer Society Press

Full text available:  pdf(427.10 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citings](#), [index terms](#)

This tutorial covers the circuit fundamentals of CMOS circuits which contribute to the consumption of energy in portable products, as well as guidelines for the design of systems in order to reduce energy consumption and prolong battery life. Circuit fundamentals will include a definition of terms, basic circuit elements, laws of operation, and basic circuit theory applying energy consumption. We will then present three major principles of energy reduction: reducing number of transitions, r ...

#### 5 Digital storage using ferromagnetic materials



A. E. De Barr, R. Millership, P. F. Dorey, R. C. Robbins, P. D. Atkinson

May 1952 **Proceedings of the 1952 ACM national meeting (Pittsburgh)**

**Publisher:** ACM Press

Full text available:  pdf(1.81 MB) Additional Information: [full citation](#), [abstract](#), [references](#)

Advances in the theory of ferromagnetism and, more particularly, increased control of production process, have led to the development of greatly improved magnetic materials and have made it possible to consider the use of ferromagnetic elements or devices for many types of circuit application. They would seem particularly attractive as circuit elements for digital computers and, in particular for storage systems. In this short paper a brief account will be given of four types of digital storage ...

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John Colter, Netscape Navigator

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Volume 39, Issue 5, May 1990 Page(s):702 - 706  
Digital Object Identifier 10.1109/12.53584  
[AbstractPlus](#) | Full Text: [PDF\(360 KB\)](#) IEEE JNL  
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- ☐ **2. Algorithmic synthesis of MVL functions for CCD Implementation**  
Abd-El-Barr, M.H.; Vranesic, Z.G.; Zaky, S.G.;  
[Computers, IEEE Transactions on](#)  
Volume 40, Issue 8, Aug. 1991 Page(s):977 - 986  
Digital Object Identifier 10.1109/12.83641  
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Jain, A.K.; Bolton, R.J.; Abd-El-Barr, M.H.;  
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Jain, A.K.; Bolton, R.J.; Abd-El-Barr, M.H.;  
[Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on \[see also Circuits and Systems I: Regular Papers, IEEE Transactions on\]](#)  
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- ☐ **5. Pure phase-encoded MRI and classification of solids**  
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- ☐ **6. Partial-volume Bayesian classification of material mixtures in MR volume data using voxel histograms**  
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- ☐ **7. Heart-muscle fiber reconstruction from diffusion tensor MRI**  
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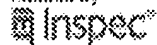
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## » Key

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IEEE JNL IEEE Journal or Magazine

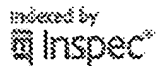
IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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## » Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

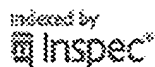
IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

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L2	34	(memory or storage or disk or drive)with ((application near2(software or code or program)) with ((heat or power or thermal)near2 (characteristic\$1 or information or parameterr\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:14
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L6	26628	"713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:47

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L7	15	I6 and ((memory or storage or disk or drive)with ((application near2(software or code or program)) with ((heat or power or thermal)near2 (characteristic\$1 or information or parameterr\$1))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:48
L8	3	I7 and (bus with(clock near3 (rate or frequenc\$3 or rate or speed)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:15
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L12	7	("5812860"   "5815693"   "5996084"   "6081901"   "6119241"   "6216235"   "6243820").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/24 16:23
L13	0	((((clock near3 (rate or frequenc\$3 or rate or speed)))near4 (manag\$4 or control\$4 or chang\$4 or adjust\$4 or switch\$4 or generat\$4 or select\$4 or caculat\$4 or estimat\$4 or determin\$4))same((application near3 (program or software or code or firmware or instruction\$1))with ((thermal or heat or power)near4 (characteristic\$1 or dissipat\$4 or genrat\$4 or parameter\$1))))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:49

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L14	26521	"710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:48
L15	99776	"370"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:48
L16	66716	"375"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:48
L17	112715	"455"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:48
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L20	1	I16 and ((memory or storage or disk or drive)with ((application near2(software or code or program)) with ((heat or power or thermal)near2 (characteristic\$1 or information or parameterr\$1))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:50
L21	4	I17 and ((memory or storage or disk or drive)with ((application near2(software or code or program)) with ((heat or power or thermal)near2 (characteristic\$1 or information or parameterr\$1))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:50

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L22	0	I14 and (((clock near3 (rate or frequenc\$3 or rate or speed))near4 (manag\$4 or control\$4 or chang\$4 or adjust\$4 or switch\$4 or generat\$4 or select\$4 or caculat\$4 or estimat\$4 or determin\$4))same((application near3 (program or software or code or firmware or instruction\$1))with ((thermal or heat or power)near4 (characteristic\$1 or dissipat\$4 or genrat\$4 or parameter\$1))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:49
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L24	0	I16 and (((clock near3 (rate or frequenc\$3 or rate or speed))near4 (manag\$4 or control\$4 or chang\$4 or adjust\$4 or switch\$4 or generat\$4 or select\$4 or caculat\$4 or estimat\$4 or determin\$4))same((application near3 (program or software or code or firmware or instruction\$1))with ((thermal or heat or power)near4 (characteristic\$1 or dissipat\$4 or genrat\$4 or parameter\$1))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:49
L25	0	I17 and (((clock near3 (rate or frequenc\$3 or rate or speed))near4 (manag\$4 or control\$4 or chang\$4 or adjust\$4 or switch\$4 or generat\$4 or select\$4 or caculat\$4 or estimat\$4 or determin\$4))same((application near3 (program or software or code or firmware or instruction\$1))with ((thermal or heat or power)near4 (characteristic\$1 or dissipat\$4 or genrat\$4 or parameter\$1))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:49